



IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

INVENTORS:

Yinon Degani
Charlie Chunlei Gao
King Lien Tai

CASE: S1

SERIAL NO.: 10/053,818

FILING DATE: 01/22/02

TITLE: TESTING INTEGRATED CIRCUITS

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EXAMINER: Nguyen, Vinh P

CHANGE OF CORRESPONDENCE ADDRESS

SIR:

Please change the correspondence address to:

Law Firm of Peter V. D. Wilde
301 East Landing
Williamsburg, VA 23185

Peter V. Wilde
Peter V.D. Wilde
Reg. No. 19658
Attorney for applicants

Date: SEP 30 2003

Law Firm of Peter V.D. Wilde
301 East Landing
Williamsburg, VA 23185